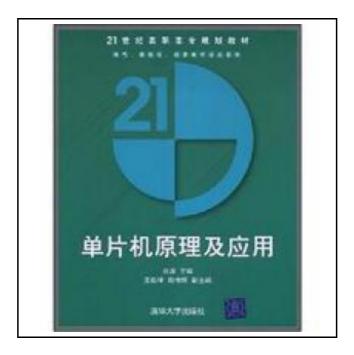
## Microcontroller Theory and Applications



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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Pages Number: 311 Publisher: Tsinghua University Press Pub. Date :2008-9-1. Introduction to the book to mcs-51 microcontroller family as an object. introduced the micro-computer hardware architecture. mcs-51 instruction set and interface technology. The book is divided into 11 chapters. including: basic knowledge of microcontroller hardware architecture. mcs-51 microcontroller instruction set. assembly language programming. mcs-51 and the timer counter interrupt system. parallel io interface expansion. chip memory expansion. man-machine interface. analog interface. serial communications. singlechip applications. This book describes the Deep, easy to understand, especially for beginners. Book for professional or vocational colleges relevant professional mechanical and electrical as technology-based courses. Microcontroller Theory and Applications of materials. engineering and technical personnel can be as self-study reference. Contents: Chapter 1 Basics 1.1 Overview 1.1.1 computer development of computer hardware architecture of microcomputers 1.1.2 1.1.3 1.2 computer terms the major technical indicators embedded systems and microcontrollers 1 .2.1 embedded microcontroller development of computer systems 1.2.2 1.2.3 1.2.4 MCU MCU features a typical single-chip products 1.2.5 Classification 1.2.6 Application of a Microcontroller .3 coded number system and number of the computer system 1.3.1 1.3.2 1.3.3 signed number system conversion. said the number of computer coding methods 1.3.4 Summary Exercises Chapter 2 singlechip microcontroller hardware architecture 2.1MCS-51 composition and internal structure 2.1.1MCS-51 singlechip microcontroller 2.1.2MCS-51 overall structure of the various features described 2.1.3CPU timing 2.1.4MCS-51 MCU reset 2.2MCS-51 microcontroller pin functions 2.3MCS-51 microcontroller memory chip memory structure 2.3.2 2.3.1 Program Memory Data Memory RAM2.3.4 ROM2.3.3 2.3.5 Special Function Registers Program Counter (SFR) 2.4MCS-51 single-chip parallel I / O interface 2.4.1PO mouth 2.4.2P1 mouth 2 mouth 2.4.3P2 mouth 2.4.4P3 .4.5 Parallel I / O interface of the load capacity Summary Exercises Chapter 3....



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